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U.S. PATENT DOCUMENTS

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/CA/	AC	5	2	1	2	5	4	8	5/18/93	DE HAAN ET AL	358	105	
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OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

/CA/	AH	"IC for Motion-Compensated De-Interlacing, Noise Reduction, and Picture-Rate Conversion, by de Haan, Transactions on Consumer Electronics, Vol. 45, pp 617-624, Aug. 1999
/CA/	AI	"A Single-Chip MPEG2 Encoder for Consumer Video Storage Applications" by Bruls et al, ICCE Digest of Technical Papers, PP 262-263, June 1997
/CA/	AJ	"Television Noise Reduction IC", by de Haan et al, IEEE Transactions on Consumer Electronics, Vol. 44, pp 143-154, Feb. 1998
/CA/	AK	"A Low-Complexity MPEG-2 Bit-Rate Transcoding Algorithm", by Gunnewiek et al, ICCCE Digest of Technical Papers, pp 316-317, June 2000
/CA/	AL	"A Low-Cost Audio/Video Single-Chip MPEG2 Encoder for Consumer Video Storage Applications", by Bruls et al, pp 314-315, IEEE 2000
/CA/	AM	"Efficient Video Coding Integrating MPEG-2 and Picture-Rate Conversion", by de Bruijn, IEEE Transactions on Consumer Electronics, Vol. 48, No. 3, pp 688-693, August 2002
/CA/	AN	"An Innovative, High Quality and Search Window Independent Motion Estimation Algorithm and Architecture for MPEG-2 Encoding", by Rovati et al, IEEE Transactions on Consumer Electronics, Vo. 46, No. 3, pp 697-705, August 2000

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